Attorney Docket No.: YOR920030258US1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application

I hereby certify that this paper is being deposited on this date with the U.S. Postal Service as first class mail addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Signature: Lisa L. Vulpis Date: January 30, 2004

Applicant(s): W. Rhee et al.
Docket No.: YOR920030258US1
Serial No.: 10/697,751
Filing Date: October 30, 2003
Group: To Be Assigned
Examiner: To Be Assigned

Title: Voltage-Controlled Delay Circuit Using Second-Order Phase Interpolation

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TRANSMITTAL OF FORMAL DRAWINGS

Mail Stop PGPUB Drawings
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Applicants submit herewith five (5) sheets of formal drawings in the above-referenced patent application.

Respectfully submitted,

William E. Lewis

Date: January 30, 2004

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